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DATE MAILED: 11/18/2003

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/738,127	12/15/2000	Larry B. Li	50944.9900	9821
75	590 11/18/2003		EXAM	INER
SCOTT A. HORSTEMEYER			DEMAKIS, JAMES A	
THOMAS, KA	YDEN, HORSTEMEYER	& RISLEYY, L.L.P.		
100 GALLERIA	A PARKWAY		ART UNIT	PAPER NUMBER
SUITE 1750			2836	
ATLANTA, G	A 30339		DATE MAN ED 11/10/000	_

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	
	09/738,127	LI, LARRY B.	
Office Action Summary	Examiner	Art Unit	
	James A Demakis	2836	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet v	vith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFF after SIX (6) MONTHS from the mailing date of this communication - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st - Any reply received by the Office later than three months after the m earned patent term adjustment. See 37 CFR 1.704(b). Status	N). R 1.136(a). In no event, however, may a b. repty within the statutory minimum of the riod will apply and will expire SIX (6) MC latute, cause the application to become a	reply be timely filed irty (30) days will be considered timely. INTHS from the mailing date of this communic ABANDONED (35 U.S.C. § 133).	ation.
1) Responsive to communication(s) filed on A	Amendment of 7/14/2003.		
2a) ☐ This action is FINAL . 2b) ☑ T	his action is non-final.		
3) Since this application is in condition for allocation closed in accordance with the practice und			ts is
Disposition of Claims			
4) ⊠ Claim(s) 1-19 is/are pending in the applicate 4a) Of the above claim(s) is/are with 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-19 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction are	drawn from consideration.		
Application Papers			
9)☐ The specification is objected to by the Exan	niner.		
10) The drawing(s) filed on is/are: a)	accepted or b) objected to	by the Examiner.	
Applicant may not request that any objection to	• • •	• •	
Replacement drawing sheet(s) including the co			
11) The oath or declaration is objected to by the	e Examiner. Note the attach	ed Office Action or form PTO-152	2.
Priority under 35 U.S.C. §§ 119 and 120			
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the papplication from the International But * See the attached detailed Office action for a 13) Acknowledgment is made of a claim for dom since a specific reference was included in the 37 CFR 1.78. a) The translation of the foreign language 14) Acknowledgment is made of a claim for dom reference was included in the first sentence of	nents have been received. nents have been received in priority documents have bee reau (PCT Rule 17.2(a)). list of the certified copies no restic priority under 35 U.S.C refirst sentence of the specific reprovisional application has restic priority under 35 U.S.C	Application No n received in this National Stage t received. \$\cdot\\$\ \\$\ 119(e)\ (to a provisional application or in an Application Data state) been received. \$\cdot\\$\\$\ 120\ \text{and/or 121 since a specification or in an Application Data state}	cation) Sheet. cific
Attachment(s)			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No() 5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-152)	- ·

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-7,9-17,19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Iwai et al (USPN 5670885) in view of Haas, Jr. et al (USPN 5392185) and Huard (USPN 4875130). Regarding claims 1-7,11,13 -15,17,19:

Iwai et al disclose a protective element transistor device 1 connected to the input and output terminals of a semiconductor internal circuit 2 to be protected from ESD by discharging the energy to ground. The protective transistor device is an NPN bipolar, which has the collector tied to ground and the base connected through a resistance value to ground also, is operated as an inverse vertical bipolar transistor, see Col.14, lines 50-64 and Col. 16, lines 39-56, and Figure 5a.

Iwai et al do not disclose a transmission line coupling to an internal circuit to be protected for use at RF frequencies of 1-2 Ghz.

Although it is well known that circuits required to operate at high frequencies or RF frequencies need interconnections and couplings designed for transmission lines; these circuits can be utilized with transistors and other circuit elements as at lower frequencies.

Haas, Jr. et al disclose ESD protection circuits for RF applications using bipolar transistors and having improved characteristics of lower capacitance and high switching speeds,

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see "Summary of the Invention" and "Detailed Description of Preferred Embodiments", Section 3, lines 1-29.

It would have been obvious to one having ordinary skill in the art at the time of the invention to have modified Iwai et al for use with a transmission line because the protection of an RF circuit would be analogous to a lower frequency circuit.

Regarding claims 9-10,12,16:

Although it is well known to require the protection device to operate above the normal operating voltages of the circuit that is to be protected, it is also well known and obvious that transistors operate at 5 volts and below.

Huard also treats and discloses typical breakdown voltages and operating voltages, see "Background of the Invention" and "Detailed Description" sections. Furthermore, it would appear straight forward to one of ordinary skill in the art that the voltage that the protection circuit begins to function, has to be higher than the operating voltage of the circuit it is protecting; and the breakdown voltage of the protection transistor must be less than that of the following circuit requiring protection. For a protection transistor to operate at or near 5 volts would not be unusual, since most IC/ transistor circuits can operate at voltages below that, typically between 2-4 volts. For a person of ordinary skill in the art at the time of the invention, it would have been obvious to use 5 volts.

Regarding claims 8,18:

The use of PNP devices in place of NPN devices is well known and obvious.

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Haas Jr et al discloses the use of PNP bipolar junction transistors in ESD protection

circuits, see "Detailed Description of Preferred Embodiments", section 3, lines14-30 and section

5, line 56; and "Abstract".

It would have been obvious to one having ordinary skill in the art at the time of the

invention that PNP transistors could be used in place of NPN transistors with adjustments for

polarity differences.

Response to Arguments

3. Applicant's arguments with respect to claims 1-2,4-7,9-10,12-14,16-17,19 have been

considered but are most in view of the new ground(s) of rejection.

The objection of claims 3,8,11,15,18 is withdrawn.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to James A Demakis whose telephone number is 703.305.7938.

The examiner can normally be reached on 7:30AM-4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Brian Sircus can be reached on 703.308.3119. The fax phone number for the

organization where this application or proceeding is assigned is 703.872.9306.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is 703,308,0956.

James A. Demakis

11/14/03

STEPHEN W. JACKSON

PRIMARY EXAMINER

Hephen Jackson

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